[02885/77]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s)	:	Martin VORBACH et al.
Serial No.	:	10/764,159
Filing Date	:	January 23, 2004
For	:	METHOD OF HIERARCHICAL CACHING OF CONFIGURATION DATA HAVING DATAFLOW PROCESSORS AND MODULES HAVING TWO- OR MULTIDIMENSIONAL PROGRAMMABLE CELL STRUCTURE (FPGAs, DPGAs, etc.)
Group Art Unit	:	2186
Examiner	:	Stephen C. Elmore
Confirmation No.	:	8160

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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on

Date 3 May 05   Atty's Reg. # 36,098

Atty's Signature
MICHELLE M. CARNIAUX
KENYON & KENYON

AMENDMENT

SIR:

This paper addresses the Office Action of November 4, 2004. Initially, please amend the above-captioned application without prejudice as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are found in the listing of claims, which begins on page 3 of this paper.

Remarks begin on page 4 of this paper.